

Professor Name

Hiroyasu Ishikawa (Mr.)

Laboratory name

Semiconductor Electronics Laboratory

Laboratory Website

<https://www.shibaura-it.ac.jp/en/research/laboratory/00224.html>

Keyword

Semiconductors, deposition, CVD, Sputtering, Photolithography, Solar cells, HEMTs

Tasks in the laboratory

You will learn about the current state of the research field, practice basic research routines and discuss future research topics on non-Si photovoltaic materials.

Program period

At least 1 month

Required skills

Electronic or electric engineering students and graduates are expected.

Desired skills (Preferred skills)

Skills on growth/deposition of semiconductors and related materials, and semiconductor device fabrication

Eligibility-school year

- Bachelor 3rd
- Bachelor 4th or higher
- Master 1st
- Master 2nd
- PhD 1st
- PhD 2nd
- PhD 3rd

Eligibility-student's major/fields

- Mechanical
- Chemistry
- Material
- Electrical
- Electronic
- Computer Science
- Life Science
- Mathematical
- Civil Engineering
- Architecture
- Engineering and design
- Other